

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings of claims in the application:

**Listing of Claims:**

1. (Canceled)

2. (Canceled)

3. (Canceled)

4. (Canceled)

5. (Currently amended) The method of claim 4 A method for making an opening for electrical contact, the method comprising:

performing a first etch through a first dielectric layer to form a first via and a second dielectric layer;

filling the first via with a BARC material to form a first BARC layer;

performing a second etch on the first BARC layer to form a second BARC layer, the second etch having a first etch rate in a first peripheral region of the second BARC layer and a second etch rate in a first central region of the second BARC layer, the first peripheral region located around a sidewall of the first via, the first central region located around a center of the first via, the first etch rate being larger than the second etch rate, the first peripheral region located lower than the first central region, a first top surface of the second BARC layer having substantially a first convex shape, wherein the second etch includes a dry etch, the dry etch using a plurality of ions, the plurality of ions having a plurality of velocities respectively, the plurality of velocities having a plurality of angles with respect to a direction vertical to a top surface of the dielectric layer respectively, an average magnitude of the plurality of angles being smaller than 10 degrees;

performing a third etch through a second dielectric layer to form a trench and a third BARC layer, the trench having a trench bottom surface, the trench bottom surface being

Response to Office Action mailed August 18, 2005

substantially free from any spike around a side surface of the third BARC layer, a second top surface of the third BARC layer having substantially a second convex shape; and  
removing the third BARC layer to form a second via, and  
wherein the second etch uses a plasma having an electron temperature, the  
electron temperature being higher than 4 eV and lower than 10 eV, and  
wherein the plasma has a gas pressure, the gas pressure being lower than 40  
mTorr.

6. (Original) The method of claim 5 wherein the average magnitude of the plurality of angles being smaller than 5 degrees.

7. (Original) The method of claim 6 wherein the second etch uses a plasma, the plasma having a plasma density, the plasma density exceeding  $5 \times 10^{16}$  ions/m<sup>3</sup>.

8. (Currently amended) The method of claim [[1]] 5 wherein the second etch uses an oxygen gas.

9. (Currently amended) The method of claim [[1]] 5 further comprising filling the trench and the second via with a conductive material.

10. (Currently amended) The method of claim [[1]] 5 wherein the first dielectric layer comprises at least one selected from silicon oxide, FSG, and silicon nitride.

11. (Canceled)

12. (Currently amended) The method of claim [[11]] 15 wherein the second etch uses a plasma, the plasma having an electron temperature, the electron temperature being higher than 4 eV.

13. (Original) The method of claim 12 wherein the electron temperature is lower than 10 eV.

Response to Office Action mailed August 18, 2005

14. (Original) The method of claim 12 wherein the plasma has a gas pressure, the gas pressure being lower than 40 mTorr.

15. (Currently amended) The method of claim 11 A method for making an electrical contact, the method comprising:

performing a first etch through a first protective layer and a first dielectric layer to form a first via, a second protective layer and a second dielectric layer, the first protective layer located on the first dielectric layer;

filling the first via with a BARC material to form a first BARC layer;

performing a second etch on the first BARC layer to form a second BARC layer, the second etch having a first etch rate in a first peripheral region of the second BARC layer and a second etch rate in a first central region of the second BARC layer, the first peripheral region located around a sidewall of the first via, the first central region located around a center of the first via, the first etch rate being larger than the second etch rate, the first peripheral region located lower than the first central region, wherein the second etch includes a dry etch, the dry etch using a plurality of ions, the plurality of ions having a plurality of velocities respectively, the plurality of velocities having a plurality of angles with respect to a direction vertical to a top surface of the dielectric layer respectively, an average magnitude of the plurality of angles being smaller than 10 degrees;

performing a third etch through a second protective layer and a second dielectric layer to form a trench and a third BARC layer, the trench having a trench bottom surface, the trench bottom surface being substantially free from any spike around a side surface of the third BARC layer;

removing the third BARC layer to form a second via, a cross-section of the second via being smaller than a cross-section of the trench; and

performing a fourth etch through a stop layer to form a third via, the dielectric layer located on the stop layer; and filling the trench and the third via with a conductive material.

16. (Original) The method of claim 15 wherein the average magnitude of the plurality of angles being smaller than 5 degrees.

17. (Currently amended) The method of claim [[11]] 15 wherein the second etch uses a plasma, the plasma having a plasma density, the plasma density exceeding  $5 \times 10^{16}$  ions/m<sup>3</sup>.

18. (Currently amended) The method of claim [[11]] 15 wherein the first dielectric layer comprises at least one selected from silicon oxide, FSG, and silicon nitride.

19. (Currently amended) The method of claim [[11]] 15 wherein the conductive material comprises at least one selected from a group consisting of copper, aluminum, tungsten, and polysilicon.

20. (Currently amended) The method of claim [[11]] 15 wherein the protective layer comprises silicon oxynitride and the stop layer comprises silicon nitride.